

# Using the UCD3138OL64EVM-031

## User's Guide



Literature Number: SLUUA10  
October 2012

## ***Using the UCD3138OL64EVM-031***

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### **1 Introduction**

UCD3138OL64CEVM-031 EVM offers an easy to use test platform for stand-alone evaluation of Texas Instruments' UCD3138 highly integrated digital controller that is optimized for isolated power applications. Featuring a 64-pin socket and comprehensive set of test points, the EVM allows hardware and firmware engineers to manipulate signals on the different pins of the device individually and study open loop operation. The EVM also offers the flexibility to realize simple closed loop control configurations (using RC circuits to filter PWM output signals and regulate a DC voltage in closed loop control). Hardware ports available to communicate with the device include PMBus, UART and JTAG. The EVM accepts up to 5-V DC input and an on-board 3.3-V LDO can be used to bias the device. Alternately, using jumpers, the device can be biased using the PMBus communication pins.

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**NOTE:** In order to download firmware and program the UCD3138 device in UCD3138OL64EVM-031, a separate EVM from Texas Instruments known as the USB-TO-GPIO Adapter is required. The USB-TO-GPIO Adapter is NOT supplied with UCD3138OL64EVM-031 and must be purchased separately. The USB-TO-GPIO Adapter serves as a communication interface adaptor or a bridge between a host PC and the UCD3138 via a standard type-A to mini-B USB cable. The USB-TO-GPIO Adapter is listed below in the related products section.

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### **2 Description**

UCD3138OL64EVM-031 is an EVM board, to facilitate evaluation on UCD3138RGC digital power controller. This EVM provides hardware needed to evaluate UCD3138 64-pin device. All pins of UCD3138RGC are accessible through header connections, including all GPIO pins, ADC12 pins, front end pins, DPWM pins, and Fault pins, etc. Particularly, the board has terminal of PMBus, UART DB-9 RS232, and JTAG. All headers can be jumped to configure for a particular interested evaluation need.

#### **2.1 Typical Applications**

- UCD3138RGC Function Evaluation
- Firmware Debug
- Hardware Design Assistance

#### **2.2 Features**

- Drop-In Socket to Flexible Change the Device
- External 5.0-V Supply or PMBus Bias Supply
- JTAG Connector
- UART Connector, DB-9, RS232
- LED Indicators
- PMBus Connector to PC Computer Connection Through USB-to-GPIO Adapter
- Rich Test Points to Facilitate the Device Evaluation, System Design and Circuit and Firmware Debugging
- Board with Jumpers to Make Flexible Configuration to Adapt Various Evaluation Target

### 3 Specifications

**Table 1. UCD3138OL64EVM-031 Specifications**

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Connector J1					
JTAG	Standard JTAG communication connection	Standard			
Connector J3					
PMBus connector	Port of connection to USB-to-GPIO, pin definition refer to TI standard USB-to-GPIO document SLLU093	Standard			
Connector J4					
UART	Female DB-9, RS232	Standard			
Connector J5					
JTAG	Standard JTAG communication connection	Standard			
Connector J38					
J38-4	Port to use 3.3 V on board to bias external circuit	3.25	3.30	3.45	V <sub>DC</sub>
J38-3	External 5.0 V to board	4.90	5.00	5.10	
J38-2	External 3.3 V to board	3.27	3.30	3.33	
J38-1	Digital ground DGND				
Operation Environment					
Operating temperature range	Natural convection		25		°C
Mechanical Characteristics					
Dimensions	Width		4.0		inches
	Length		6.0		
	Component height		0.5		
Firmware for Testing					
Filename	PWR031_Test_GPIO_ADC12.x0				

## 4 Schematics

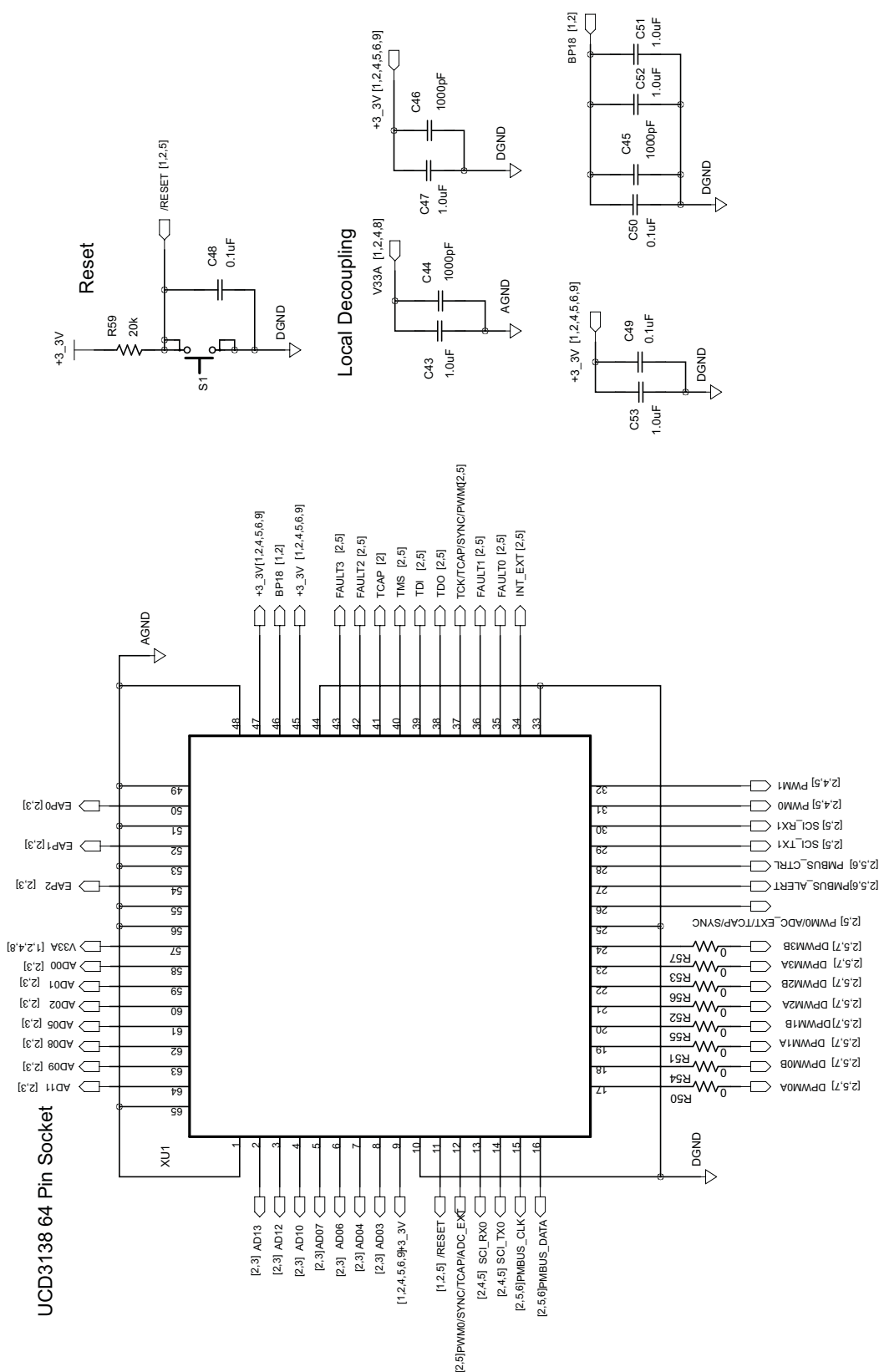


Figure 1. UCD3138OL64EVM-031 Schematics (Socket) 1 of 9

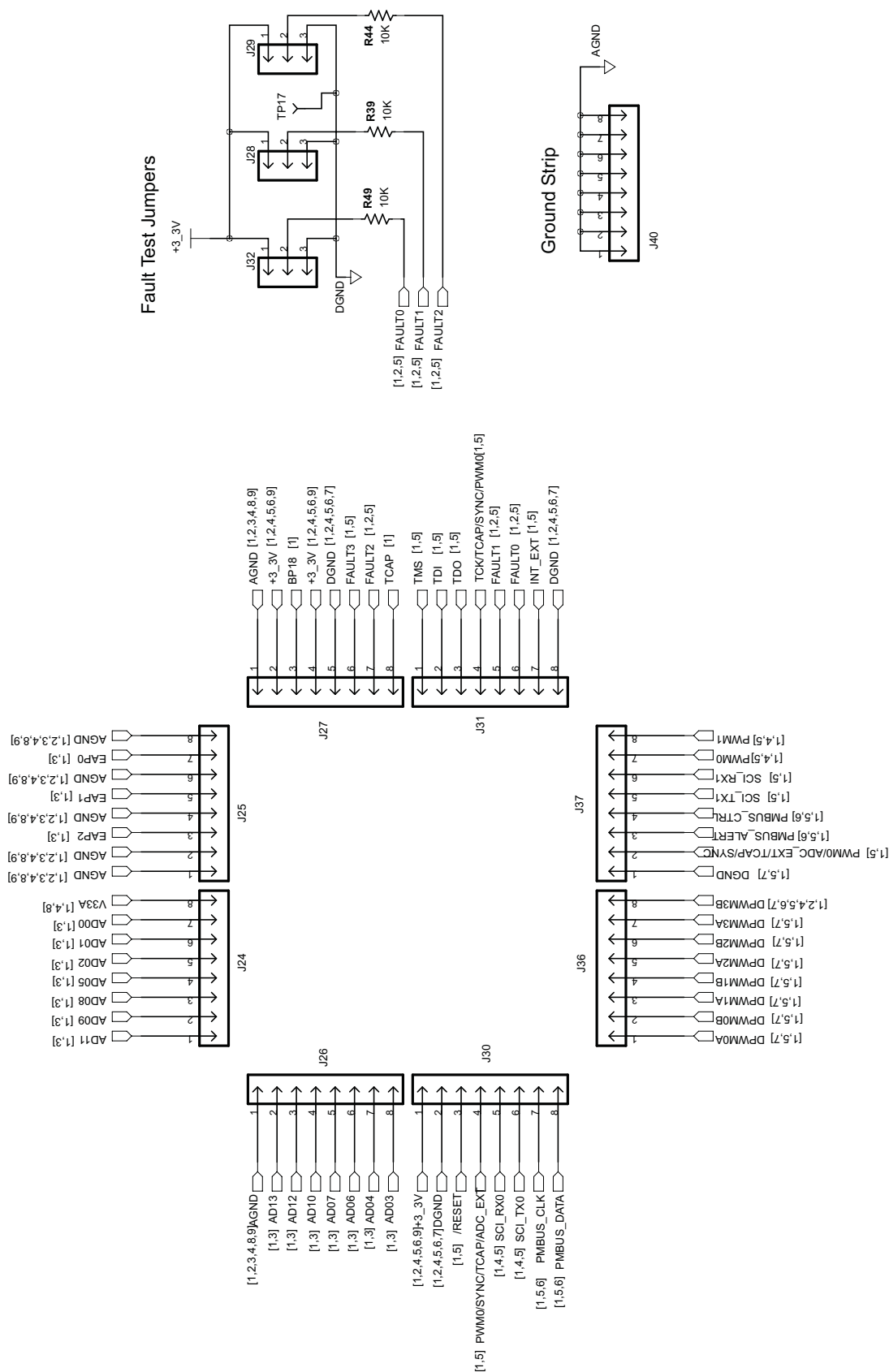
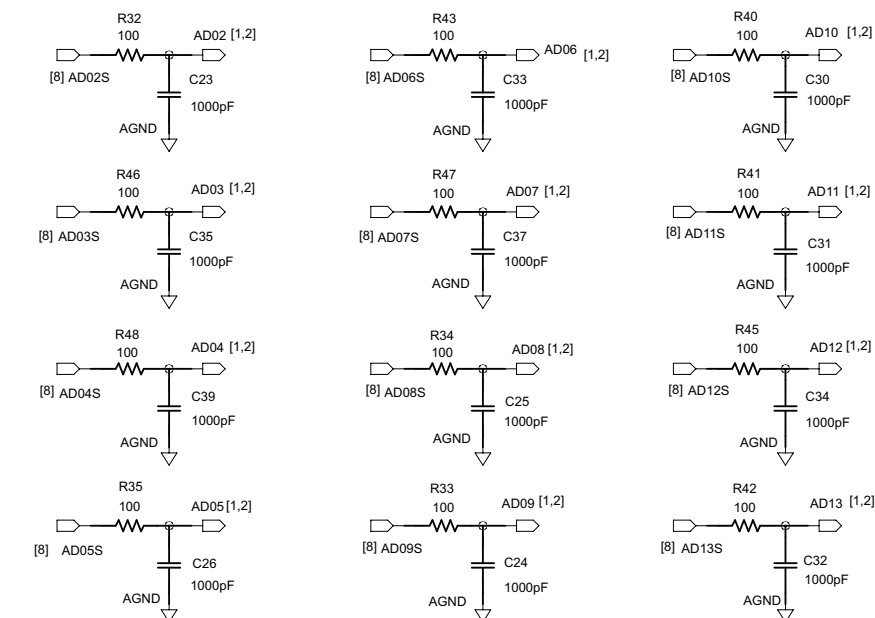
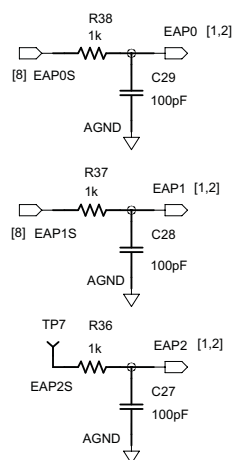


Figure 2. UCD3138OL64EVM-031 Schematics (Headers) 2 of 9

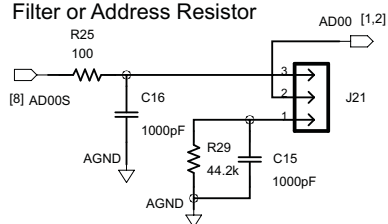
### ADC12 Filters



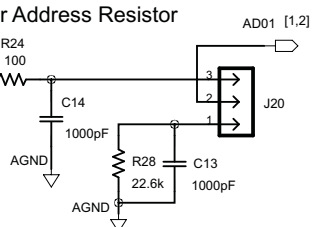
### EADC Filters



### Filter or Address Resistor



### Filter or Address Resistor



**Figure 3. UCD3138OL64EVM-031 Schematics (Filters) 3 of 9**

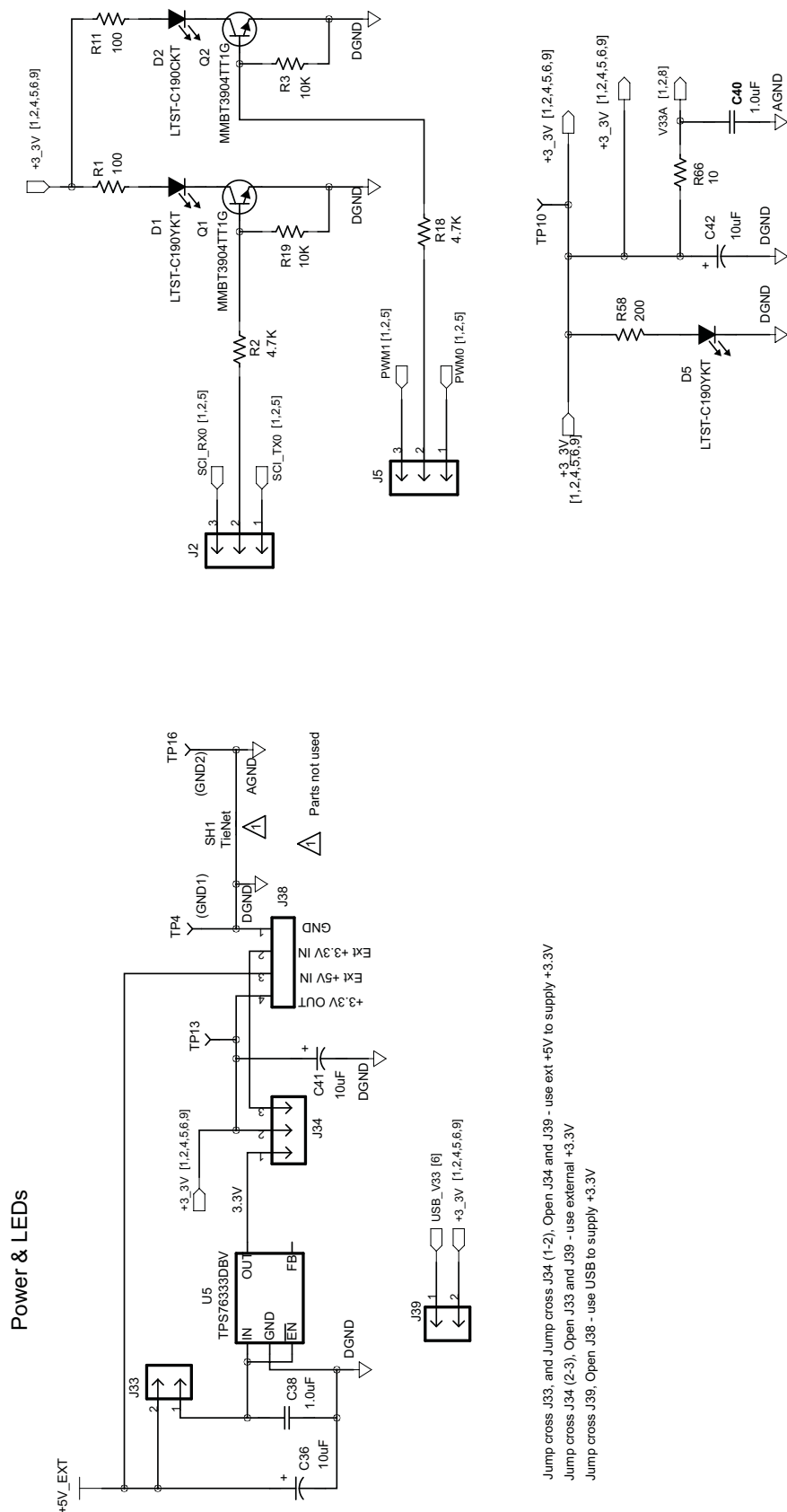


Figure 4. UCD3138OL64EVM-031 Schematics (Bias and LEDs) 4 of 9

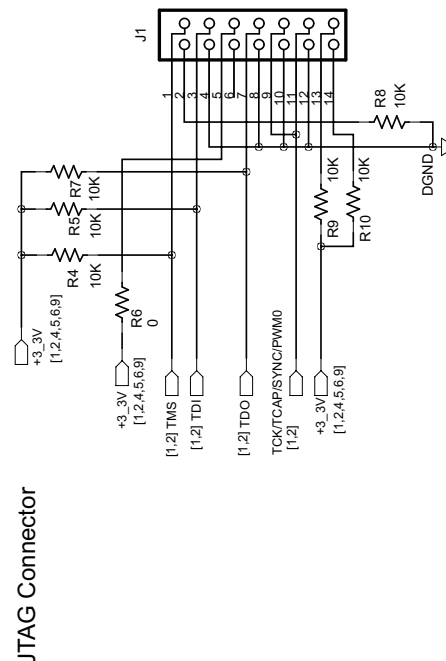
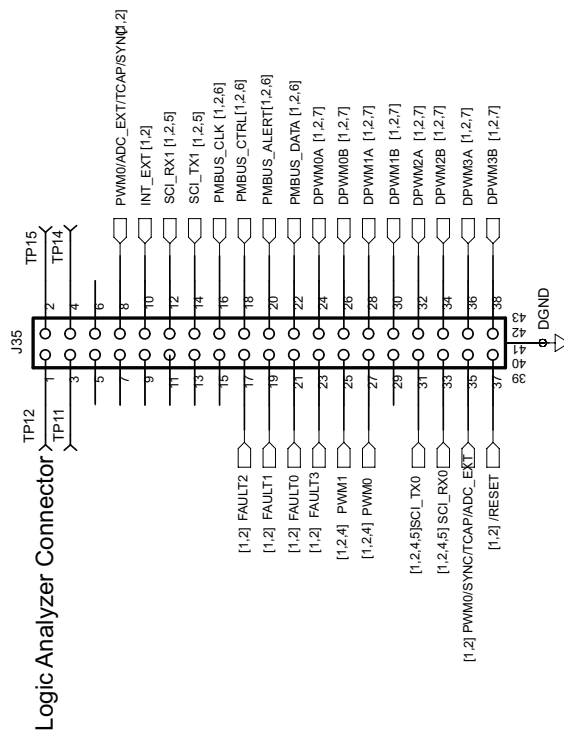
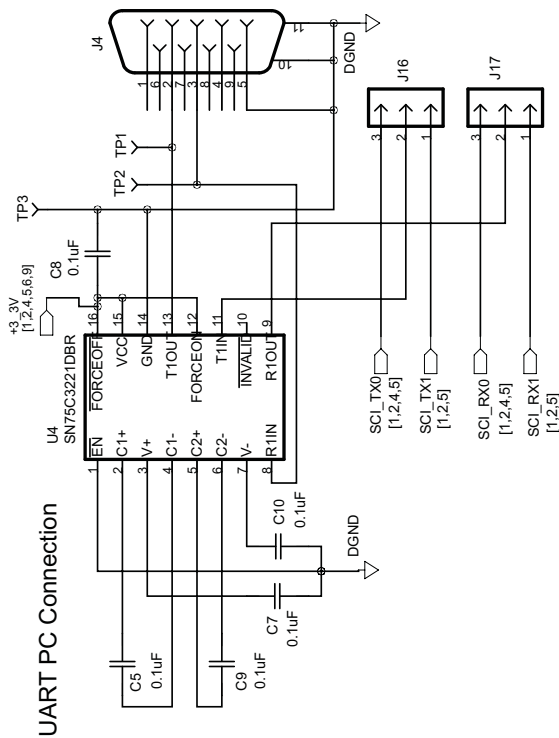
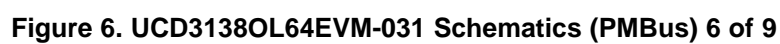


Figure 5. UCD3138OL64EVM-031 Schematics (UART and JTAG) 5 of 9





## DPWM Filters

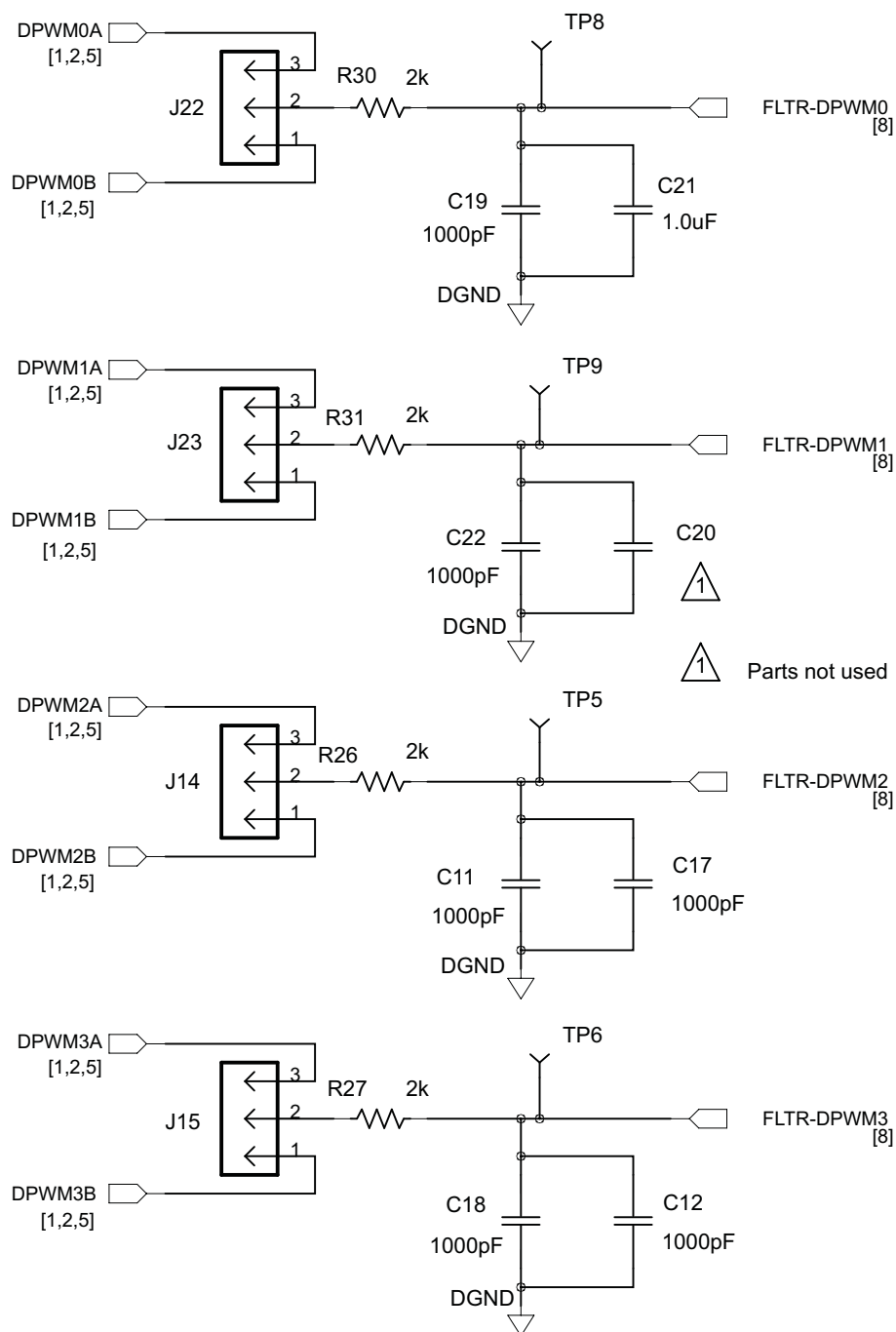
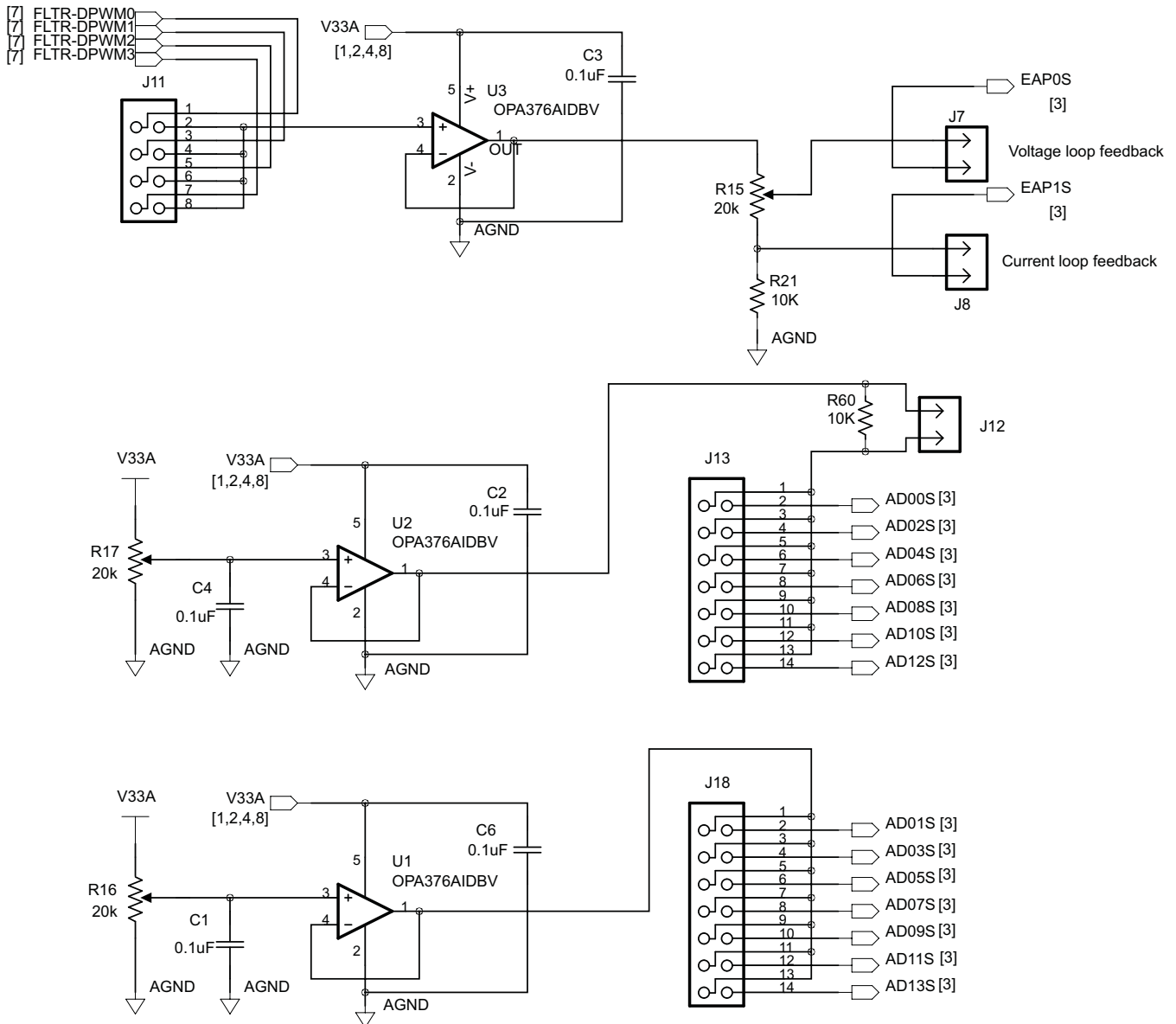
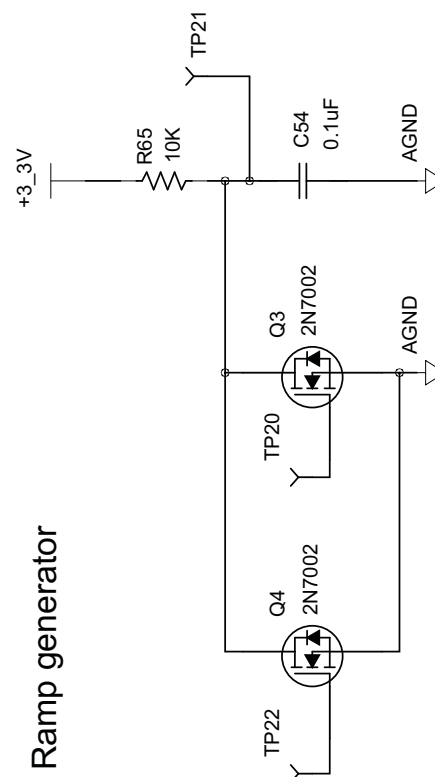
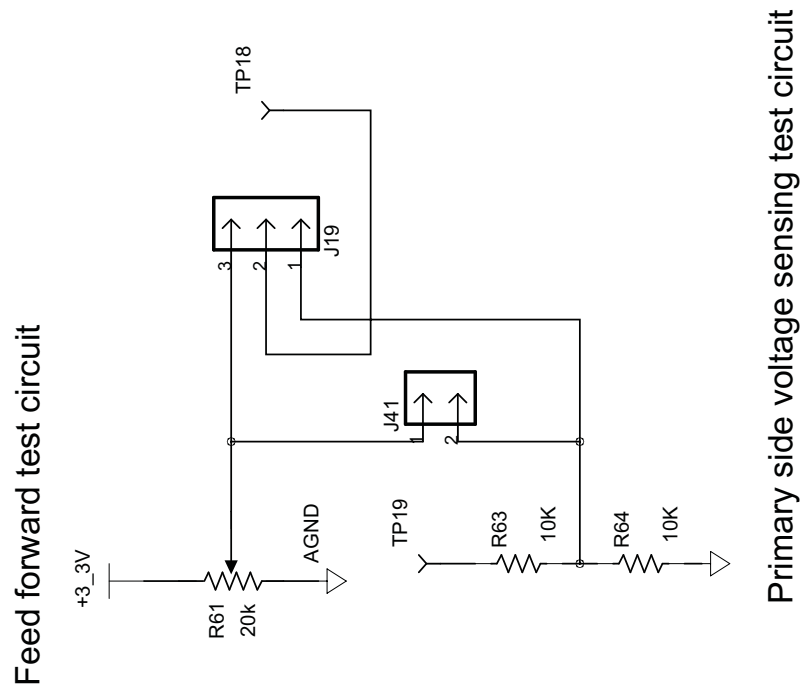


Figure 7. UCD3138OL64EVM-031 Schematics (DPWM) 7 of 9

## EADC & ADC12 Feedback



**Figure 8. UCD3138OL64EVM-031 Schematics (EADC and ADC12) 8 of 9**



**Figure 9. UCD3138OL64EVM-031 Schematics (Misc) 9 of 9**

## 5 Test Equipment

### 5.1 PC Computer

#### 5.1.1 Operating System

Microsoft Windows XP (32-bit), or Vista (32-bit), or Windows 7 (32-bit).

### 5.2 USB-to-GPIO Interface Adapter

This adapter is to establish the communication between the control card UCC3138OL64EVM-031 and the PC computer through the PMBus and the GUI, Texas Instruments Fusion Digital Power Designer.

#### 5.2.1 USB-to-GPIO Interface Adapter (HPA172)

Accessories including:

- USB interface adapter (HPA172).
- USB cable, 5-pin B mini male to type A male.
- Ribbon cable, socket-to-socket, 10-pin, 2 headers, polarized.

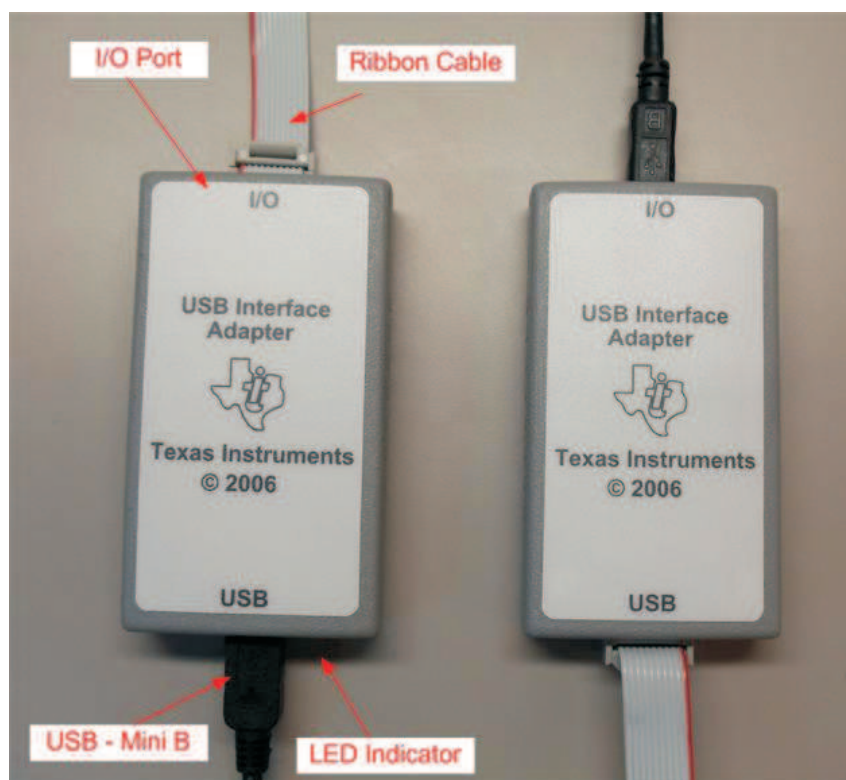


Figure 10. USB-to-GPIO Interface Adapter (HPA172)

### 5.3 Oscilloscope

An oscilloscope of analog or digital type is capable of 200-MHz bandwidth with Tektronix P6138 or equivalent oscilloscope probe.

## 6 Equipment Setup

### 6.1 GUI (Graphical User Interface)

#### 6.1.1 File for Installation

The GUI installation file is **TI-Fusion-Digital-Power-Designer-Version-1.8.92.exe** or newer version. To get a latest version of GUI, go on TI web site, [www.ti.com](http://www.ti.com), search **TI Fusion Digital Power Designer** then download and install to one's computer.

#### 6.1.2 Installation

Double click and launch the **.exe** file to start the installation. Click **Next** all the way through. When present, click **I accept the agreement** after read it. Then click **install**. After the installation, click **Finish** to exit setup. Then click **Exit Program**.

#### 6.1.3 Launch UCD3138 Device GUI

The GUI for UCD3138CC64EVM-030 board can be launched through the below steps:

Click the window **start** → click **All Programs** → click **Texas Instruments Fusion Digital Power Designer** → click **Device GUIs** → click **UCD3xxx Device GUI**.

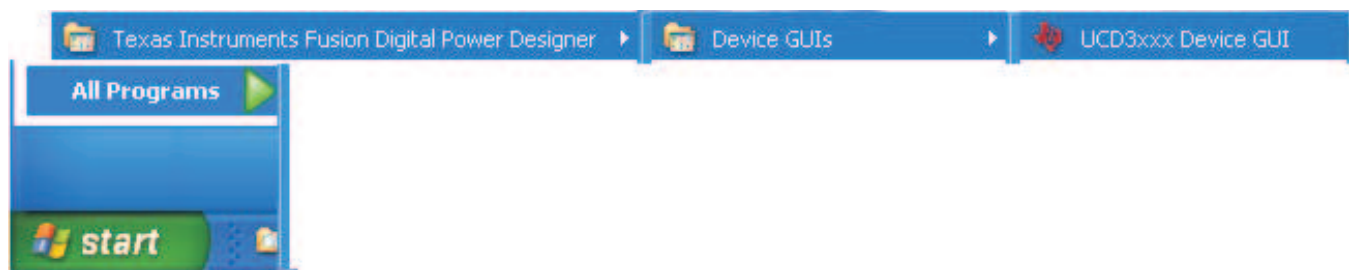


Figure 11. Device GUI Launch Path

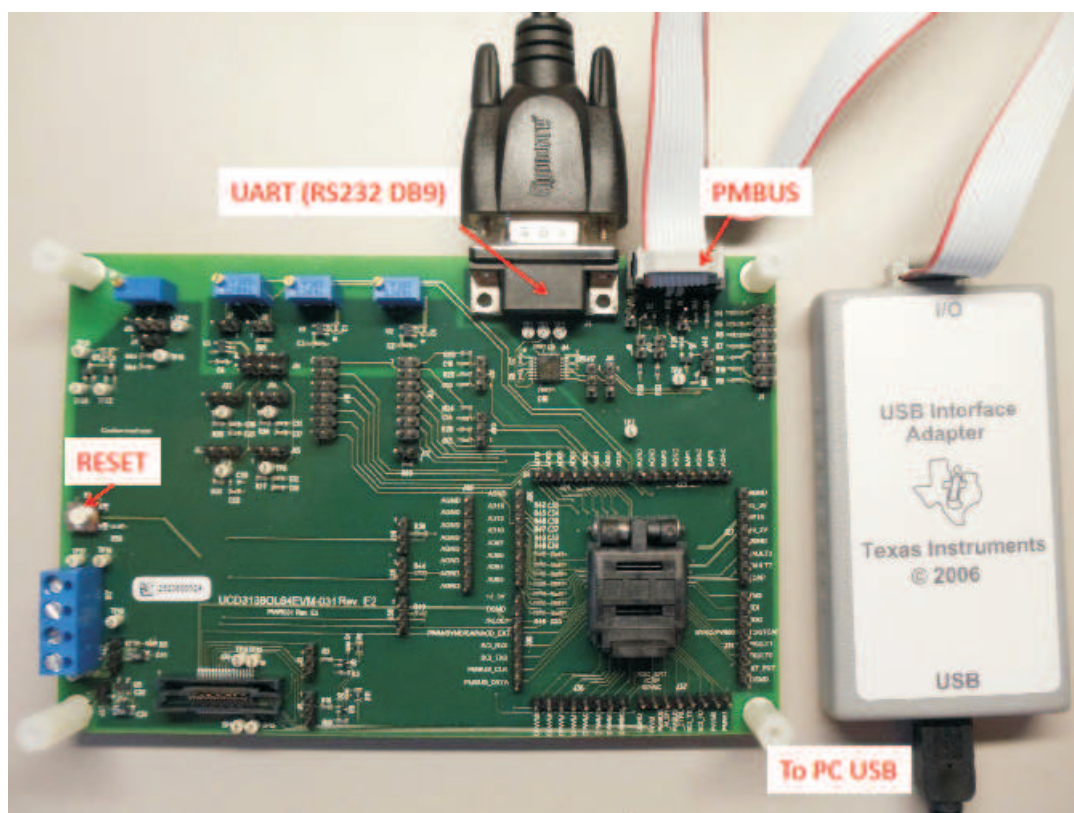
## 6.2 Hardware Setup

### 6.2.1 Setup Overview

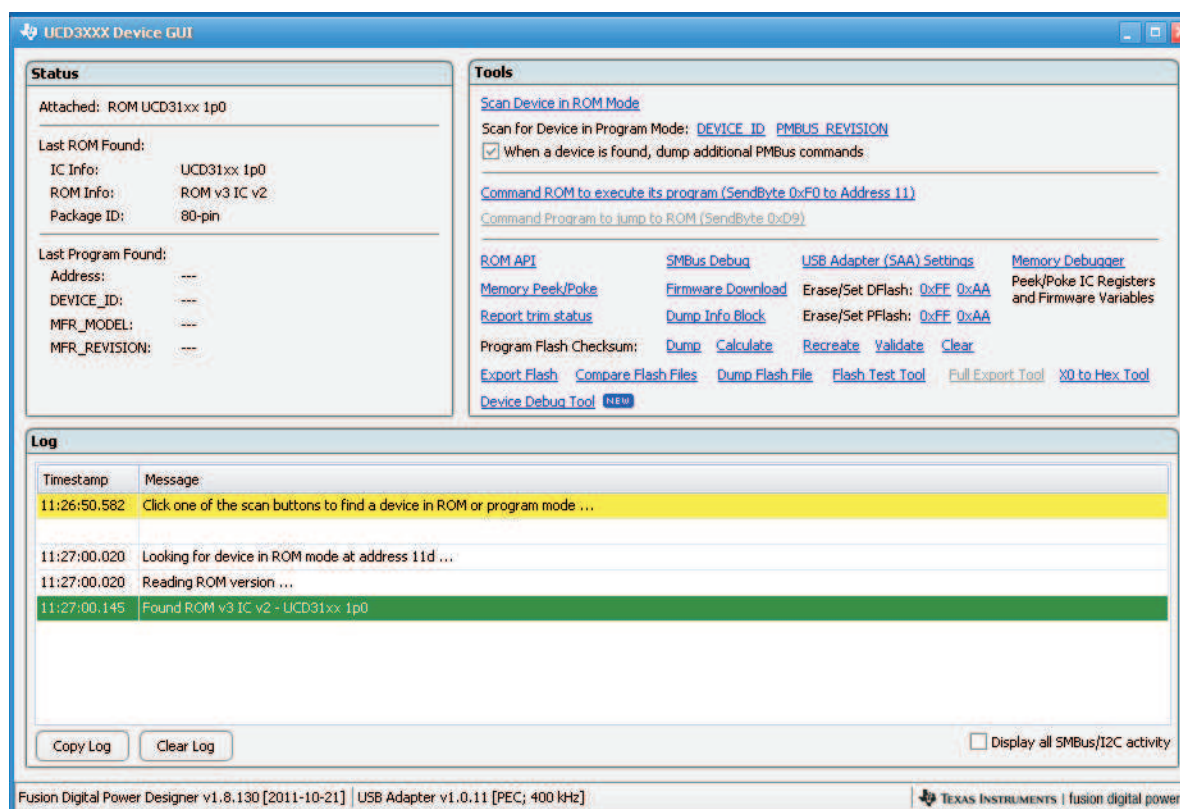
Shown below in [Figure 12](#) is the connection between UCD3138OL64EVM-031 and the PC computer through USB-to-GPIO Interface Adapter.

USB Adapter Connection

- Connect one end of the ribbon cable to the EVM, and connect the other end to the USB interface adapter
- Connect the Mini connector of the USB cable to the USB interface adapter, and connect the other end to the USB port of the PC computer.

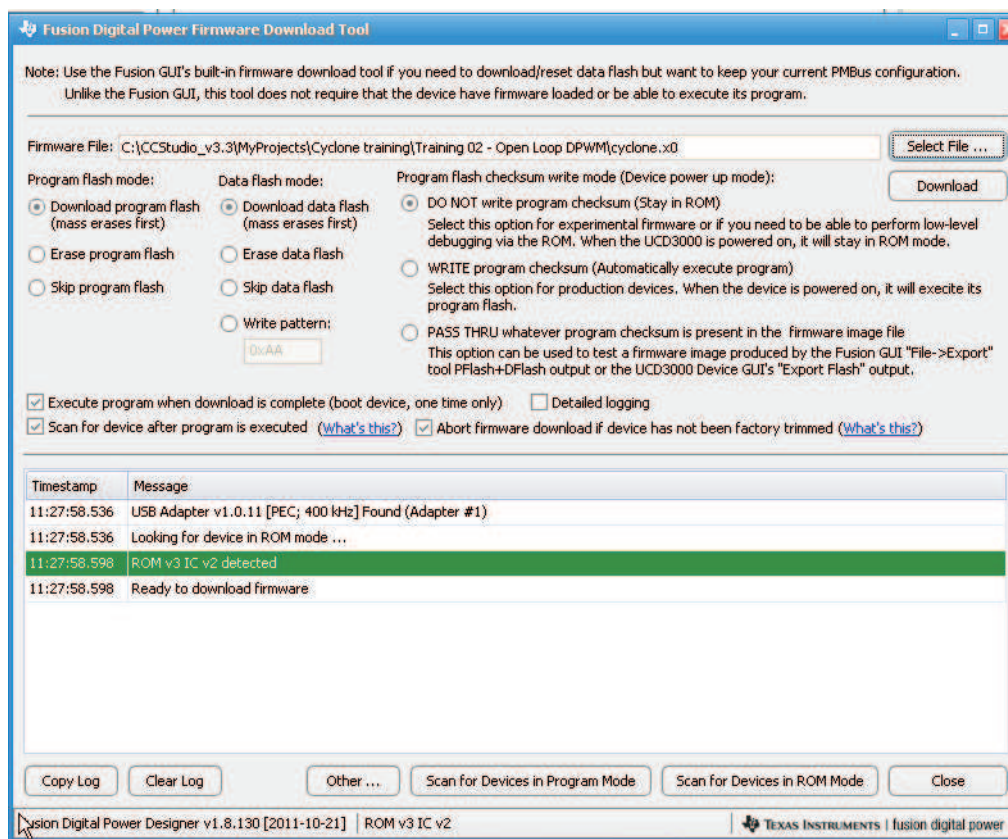


**Figure 12. UCD3138OL64EVM-031 Test Connections**



**Figure 13. UCD31xx Device GUI**





**Figure 14. Firmware Code Downloading**

### 6.3 List of Test Points

**Table 2. UCD3138OL64EVM-031 Test Point Functions**

TEST POINTS	NAME	DESCRIPTION
TP1	UART J4-2	UART transmitting
TP2	UART J4-3	UART receiving
TP3	UART GND	DGND
TP4	J38-1	DGND
TP5	FLTR-DPWM2	DPWM2 front end connection
TP6	DGND	Digital GND
TP7	EAP2S	EAP2 before filter
TP8	FLTR-DPWM0	DPWM0 front end connection
TP9	FLTR-DPWM1	DPWM1 front end connection
TP10	+3.3V	3.3 V
TP11	J35-3	Logic analyzer pin 3
TP12	J35-1	Logic analyzer pin 1
TP13	J38-4	3.3 V to outside circuit
TP14	J35-4	Logic analyzer pin 4
TP15	J35-2	Logic analyzer pin 2
TP16	AGND	AGND
TP17	DGND	DGND
TP18	FFCS	Feed forward control selection
TP19	FFC_adj	Feed forward control adjustment
TP20	Q3 Gate	Ramp generator
TP21	Q3 and Q4 drain	Ramp generator
TP22	Q4 gate	Ramp generator
J1		JTAG
J2, 5		LED D1 and D2 control
J3, 6,9,10,42		PMBus
J4,16,17		UART, DB-9 female, RS232
J7, 8,11		EADC and DPWM
J12,13,18,20,21		ADC12
J14,15, 22, 23		DPWM
J19,41		Feed forward control
J24,25,26,27,30,31,36,37		UCD3138 pinout headers
J28,29,32		Faults connections
J33,34,38,39		Board bias power management
J35		Logic Analyzer Connector, not populated
J40		AGND strip
S1		UCD3138 reset, press to reset.

## 7 Test Procedure

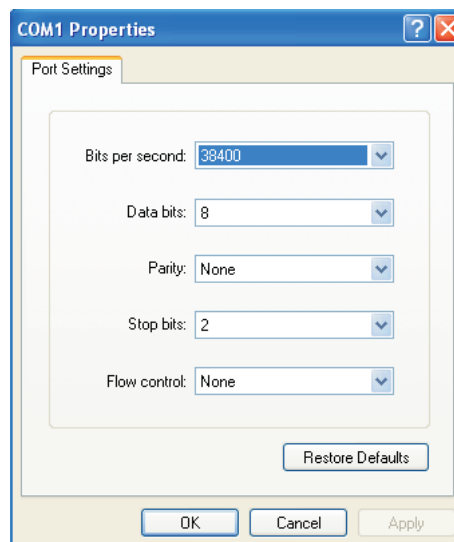
### 7.1 Download Firmware Codes to UCD3138OL64EVM-031

Set up the EVM connection based on [Figure 12](#).

1. Set up the EVM connection based on [Figure 12](#). The LED of USB adapter is lighted up.
2. Use provided jumper jump across J39. The LED of the EVM is lighted on.
3. Launch the UCD31xx device GUI following the steps described in [Section 6.2.1](#). A window shown in [Figure 13](#) appears.
4. Check **Download program flash**, **Download data flash** and **DO NOT write program checksum** as shown in [Figure 14](#).
5. Click **Firmware Download**; then a new window appears as shown in [Figure 14](#). Click **Select File** and browse an intended firmware code file with file extension **.x0**; then click **Download**. Click **Close** to exit the download window.
6. After the firmware codes downloaded to the UCD3138 device, the intended test can be performed. In this user's guide, we use **PWR031\_Test\_GPIO\_ADC12.x0** to show how to use the board.

### 7.2 Example Test and Steps

1. Setup hyper terminal using RS232 serial port with the settings shown. Then launch the hyper terminal.



**Figure 15. Hyper Terminal Settings**

2. Place jumpers on the following headers, jump across J1 pin-11 and pin-13, i.e., J1(11 and 13), J2(2 and 3), J5(1 and 2), J18(1 and 2, 3 and 4, 5 and 6, 7 and 8, 9 and 10, 11 and 12, 13 and 14), J28(1 and 2), J29(1 and 2), J32(1 and 2).
3. Connect J36 all pins together using single-in-line connector; then use a click-wire, hook-wire connect J36-1 and J30-4.
4. Place jumpers on J12(1 and 2), J16(1 and 2), J17(1 and 2), J20(2 and 3), J21(2 and 3).

- Adjust R17 to make ADC12 input to 250 mV by a voltmeter to monitor the voltage between terminal J13-1 and J40-1.

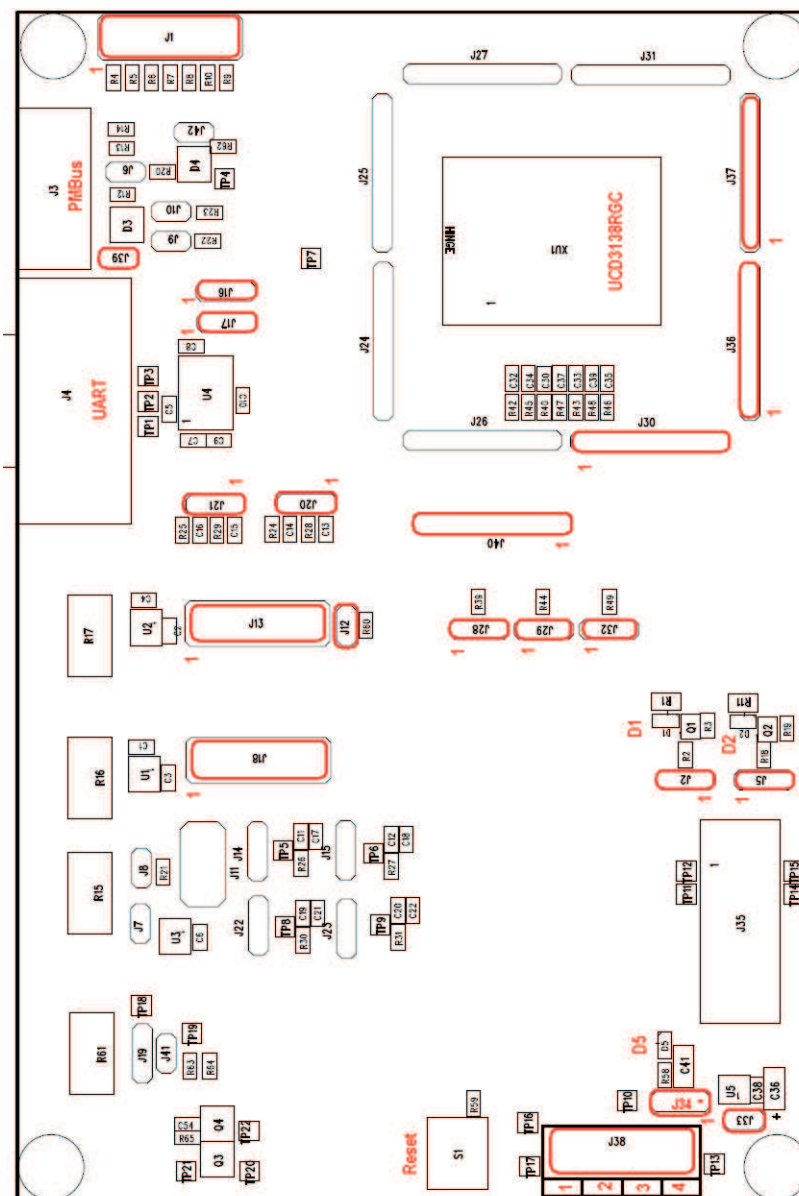
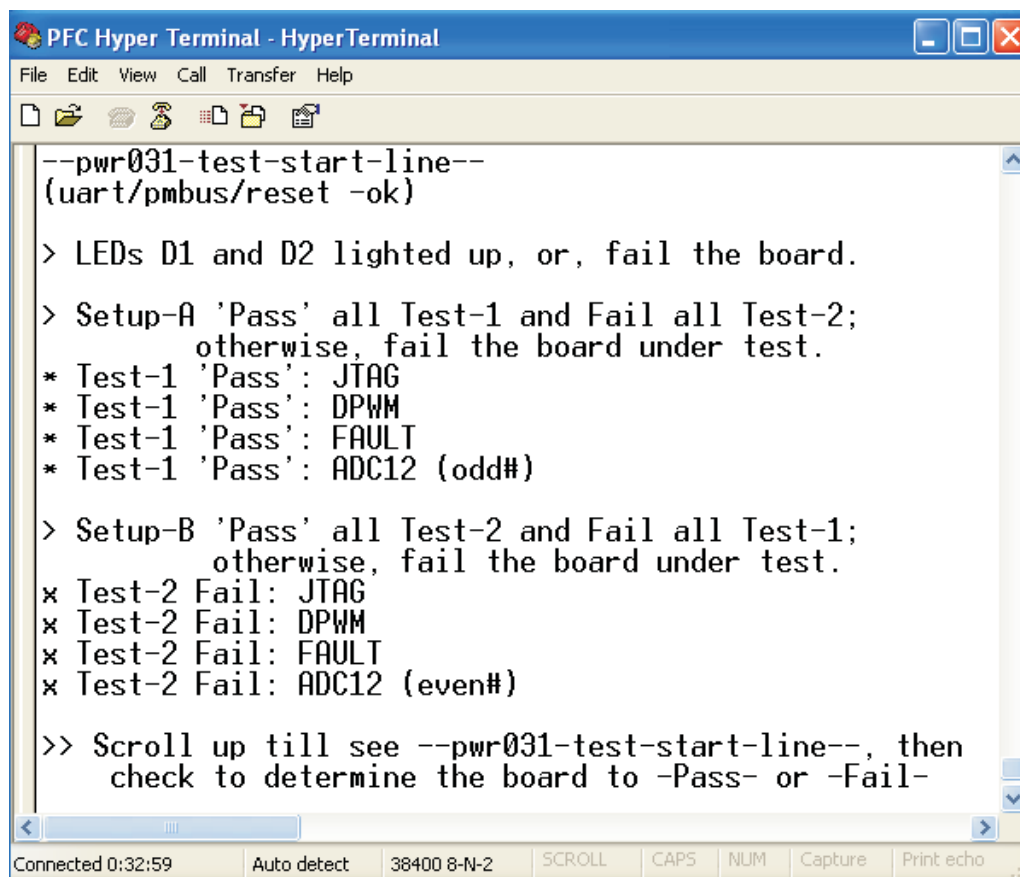


Figure 16. Location of Jumpers in Use

6. Press **Reset** button once on the board. Click **Command ROM to execute its program (sendByte0xF0 to Address 11)**. The firmware downloaded is executed, and the executed result is transmitted to the hyper terminal as shown.



The screenshot shows a HyperTerminal window titled "PFC Hyper Terminal - HyperTerminal". The window contains the following text:

```
--pwr031-test-start-line--
(uart/pmbus/reset -ok)

> LEDs D1 and D2 lighted up, or, fail the board.

> Setup-A 'Pass' all Test-1 and Fail all Test-2;
    otherwise, fail the board under test.
* Test-1 'Pass': JTAG
* Test-1 'Pass': DPWM
* Test-1 'Pass': FAULT
* Test-1 'Pass': ADC12 (odd#)

> Setup-B 'Pass' all Test-2 and Fail all Test-1;
    otherwise, fail the board under test.
x Test-2 Fail: JTAG
x Test-2 Fail: DPWM
x Test-2 Fail: FAULT
x Test-2 Fail: ADC12 (even#)

>> Scroll up till see --pwr031-test-start-line--, then
    check to determine the board to -Pass- or -Fail-
```

The status bar at the bottom of the window shows: "Connected 0:32:59", "Auto detect", "38400 8-N-2", "SCROLL", "CAPS", "NUM", "Capture", and "Print echo".

**Figure 17. Execution Result of Example Test**

### 7.3 Equipment Shutdown

1. Exit the GUI and UART.
2. Disconnect the cables.

## 8 EVM Assembly Drawing and PCB layout

The following figures (Figure 18 through Figure 23) show the design of the UCD3138OL64EVM-031 printed circuit board. PCB dimensions: L x W = 6.0 x 4/0 in, PCB material: FR4 or compatible, four layers and 1-ounce copper on each layer.

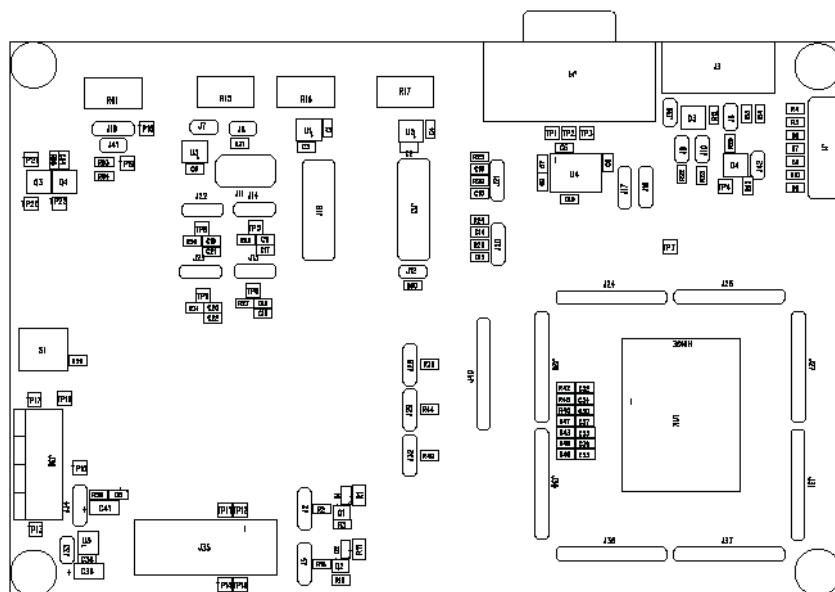


Figure 18. UCD3138OL64EVM-031 Top Layer Assembly Drawing (top view)

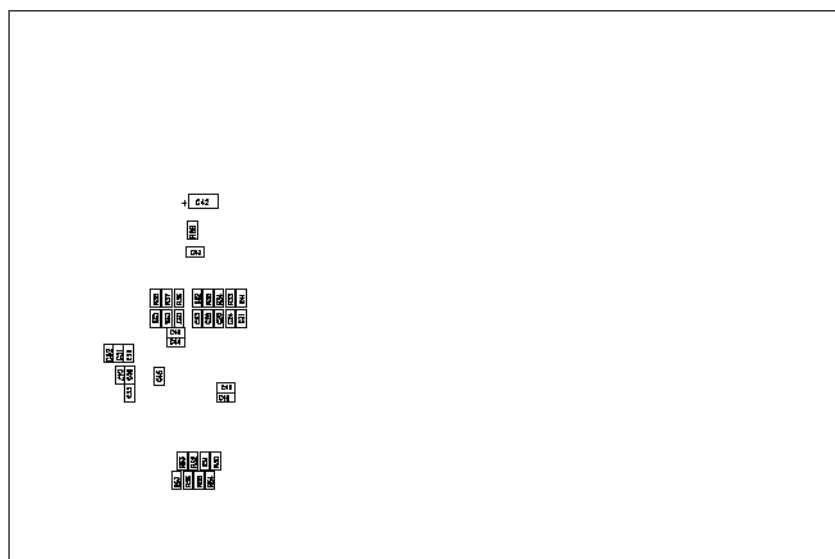


Figure 19. UCD3138OL64EVM-031 Bottom Assembly Drawing (bottom view)

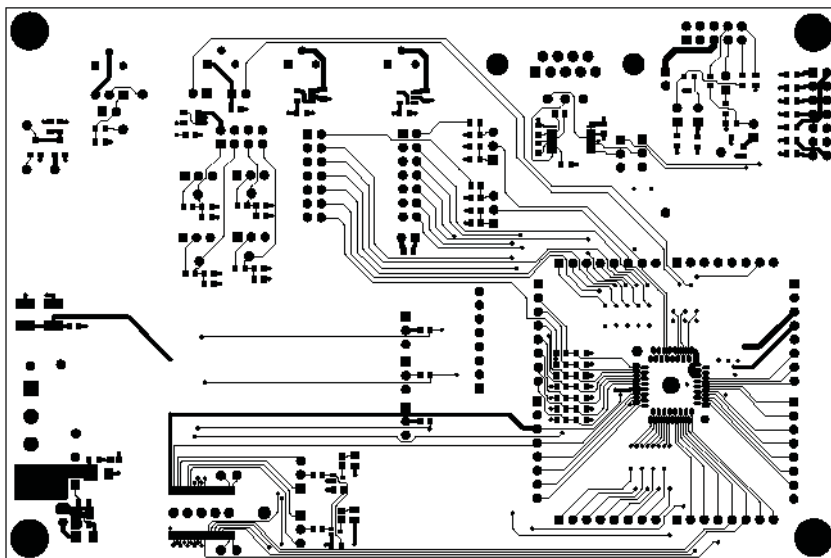


Figure 20. UCD3138OL64EVM-031 Top Copper (top view)

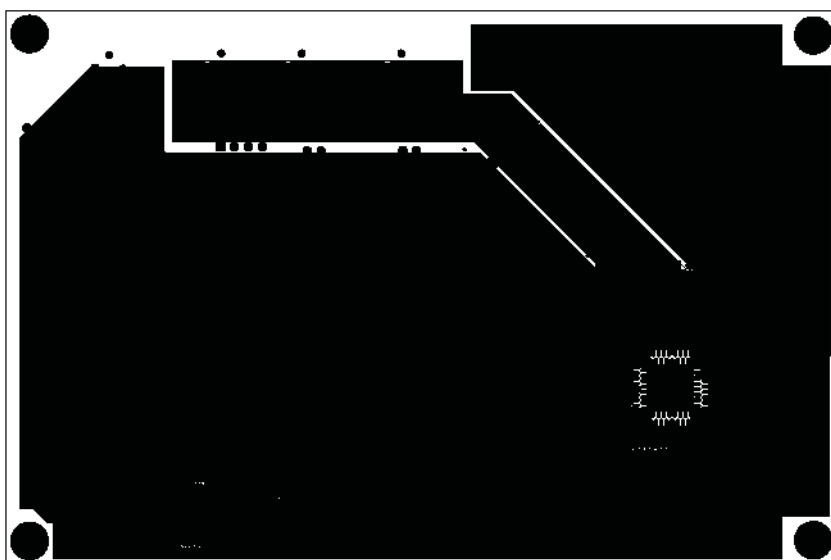


Figure 21. UCD3138OL64EVM-031 Internal Layer 1 (top view)

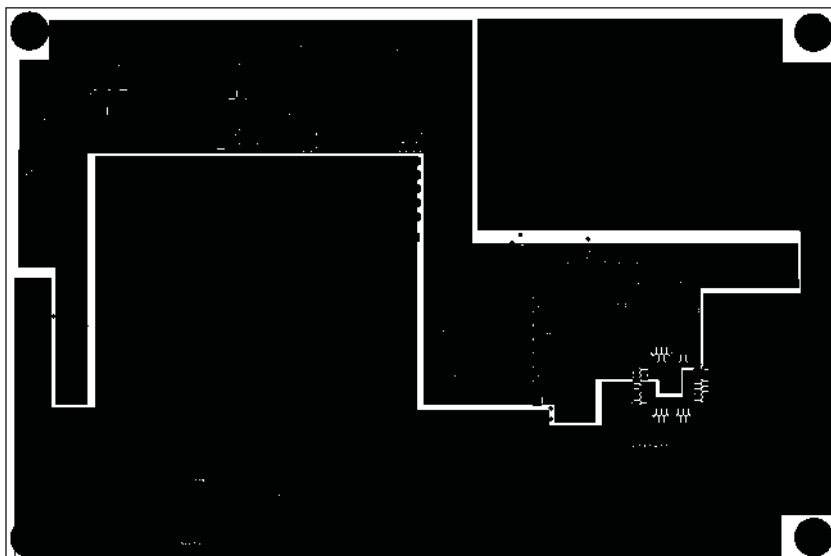


Figure 22. UCD3138OL64EVM-031 Internal Layer 2 (top view)

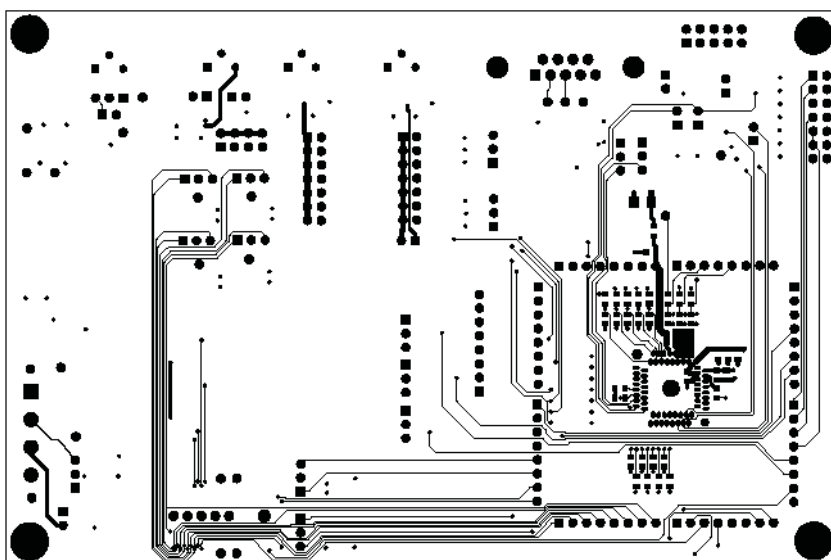


Figure 23. UCD3138OL64EVM-031 Bottom Copper (top view)



## 9 List of Materials

The EVM components list according to the schematics shown in [Figure 1](#) to [Figure 9](#)

**Table 3. UCD3138OL64EVM-031 List of Materials**

QTY	REF DES	DESCRIPTION	PART NUMBER	MFR
14	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C48, C49, C50, C54	Capacitor, ceramic, 16 V, X7R, 10%, 0.1 $\mu$ F, 0603	STD	STD
20	C11, C12, C14, C16, C17, C18, C19, C22, C23, C24, C25, C26, C30, C31, C32, C33, C34, C35, C37, C39	Capacitor, ceramic, 50 V, X7R, 10%, 1000 pF, 0603	STD	STD
5	C13, C15, C44, C45, C46	Capacitor, ceramic, 50 V, C0G, 5%, 1000 pF, 0603	STD	STD
0	C20	Capacitor, ceramic, 50 V, X7R, 10%, no population, open, 0603	STD	STD
8	C21, C38, C40, C43, C47, C51, C52, C53	Capacitor, ceramic, 16 V, X7R, 10%, 1.0 $\mu$ F, 0603	STD	STD
3	C27, C28, C29	Capacitor, ceramic, 50 V, C0G, 5%, 100 pF, 0603	STD	STD
3	C36, C41, C42	Capacitor, tantalum, 10 V, 20%, 10 $\mu$ F, 3216	293D106X0010A2T	Vishay
2	D1, D5	Diode, LED, yellow, 2.1 V, 20 mA, 6 mcd, LTST-C190YKT, 0603	LTST-C190YKT	Lite On
1	D2	Diode, LED, red, 2.1 V, 20 mA, 6 mcd, LTST-C190CKT, 0603	LTST-C190CKT	Lite On
2	D3, D4	Diode, dual, Zener, 5.1 V, 300 mW, AZ23C5V1, SOT23	AZ23C5V1-7-F	Diodes
3	J1, J13, J18	Header, 2 x 7 pin, 100-mil spacing, 0.100 inch x 2 inch x 7 inch	PEC07DAAN	Sullins
1	J11	Header, male 2 x 4 pin, 100-mil spacing, 0.20 inch x 0.40 inch	PEC04DAAN	Sullins
15	J2, J5, J14, J15, J19, J20, J21, J22, J23, J16, J17, J28, J29, J32, J34	Header, 3 pin, 100-mil spacing, 0.100 inch x 3 inch	PEC03SAAN	Sullins
9	J24, J25, J26, J27, J30, J31, J36, J37, J40	Header, male 8 pin, 100-mil spacing, 0.100 inch x 8 inch	PEC08SAAN	Sullins
1	J3	Connector, male straight 2 x 10 pin, 100-mil spacing, 4 wall, 0.338 inch x 0.788 inch	N2510-6002-RB	3M
1	J35	Plug, 40 pin with hold down, 0.272 inch x 1.000 inch	2-5767004-2	Tyco
1	J38	Terminal block, 4 pin, 15 A, 5.1 mm, 0.80 inch x 0.35 inch	ED120/4DS	OST
1	J4	Connector, 9 pin D, right angle, female, 1.213 inch x 0.510 inch	182-009-212-171	Norcomp
10	J6, J7, J8, J9, J10, J12, J33, J39, J41, J42	Header, 2 pin, 100-mil spacing, 0.100 inch x 2 inch	PEC02SAAN	Sullins
2	Q1, Q2	Bipolar, NPN, 40 V, 200 mA, 200 mW, SC-75	MMBT3904TT1G	On Semi
2	Q3, Q4	MOSFET, N-channel, 60 V, 115 mA, 1.2 $\Omega$ , SOT23	2N7002-7-F	Diodes
2	R1, R11	Resistor, chip, 1/10 W, 1%, 100 $\Omega$ , 0805	STD	STD
4	R12, R13, R14, R20	Resistor, chip, 1/16 W, 5%, 33 $\Omega$ , 0603	STD	STD
4	R15, R16, R17, R61	Potentiometer, 3/8 cermet, single turn, right angle, 20 k $\Omega$ , 0.288 inch x 0.600 inch	3296Y-1-203 LF	Bourns
2	R2, R18	Resistor, chip, 1/10 W, 1%, 4.7 k $\Omega$ , 0603	STD	STD
3	R22, R23, R62	Resistor, chip, 1/16 W, 5%, 1.5 k $\Omega$ , 0603	STD	STD

**Table 3. UCD3138OL64EVM-031 List of Materials (continued)**

QTY	REF DES	DESCRIPTION	PART NUMBER	MFR
14	R24, R25, R32, R33, R34, R35, R40, R41, R42, R43, R45, R46, R47, R48	Resistor, chip, 1/16 W, 1%, 100 $\Omega$ , 0603	STD	STD
4	R26, R27, R30, R31	Resistor, chip, 1/10 W, 1%, 2 k $\Omega$ , 0603	STD	STD
1	R28	Resistor, chip, 1/16 W, 5%, 22.6 k $\Omega$ , 0603	STD	STD
1	R29	Resistor, chip, 1/16 W, 5%, 44.2 k $\Omega$ , 0603	STD	STD
16	R3, R4, R5, R7, R8, R9, R10, R19, R21, R39, R44, R49, R60, R63, R64, R65	Resistor, chip, 1/16 W, 1%, 10 k $\Omega$ , 0603	STD	STD
3	R36, R37, R38	Resistor, chip, 1/16 W, 1%, 1 k $\Omega$ , 0603	STD	STD
1	R58	Resistor, chip, 1/16 W, 5%, 200 $\Omega$ , 0603	STD	STD
1	R59	Resistor, chip, 1/16 W, 5%, 20 k $\Omega$ , 0603	STD	STD
9	R6, R50, R51, R52, R53, R54, R55, R56, R57	Resistor, chip, 1/16 W, 1%, 0 $\Omega$ , 0603	STD	STD
1	R66	Resistor, chip, 1/16 W, 1%, 10 $\Omega$ , 0603	STD	STD
1	S1	Switch, SPST, pb momentary, sealed washable, 0.245 inch x 0.251 inch	KT11P2JM34LFS	C & K
3	U1, U2, U3	Precision, Low Noise, Low Quiescent Current Op-Amp, SOT23-5	OPA376AIDBVT	TI
1	U4	RS-232 Transceivers with Auto Shutdown, SSOP-16	SN75C3221DBR	TI
1	U5	Micro-Power, 100-mA LDO Regulator, SOT23-5	TPS76333DBVT	TI
1	XU1	Socket, 64-pin Clamshell, 0.800 inch x 1.080 inch	64QN50S19090	Plastronics
1		PCB, FR-4, 0.062", PCB, 6.000 inch x 4.000 inch	PWR031	any
1	U6	Digital Power Controller, PFC-64	UCD3138RGC	TI

## Appendix A Summary of Using Code Composer Studio v3.3

In this appendix, we describe basic steps how to use Code Composer Studio v3.3, or CCS, to compile firmware for UCD3138. A design flow is described while detailed steps for firmware code creation, and firmware debugging along with hardware are obviously beyond the scope of this user's guide and this appendix.

### A.1 Set up Code Composer Studio v3.3 for UCD3138

The recommended version of Code Composer Studio is v3.3. After completing the CCS v3.3 installation, and when first time open CCS, a window as shown in [Figure 24](#) is prompted to allow users to select the required configuration. For the UCD3138 device, please select **ARM7 SIMULATOR BIG ENDIAN**. Click **ADD** and then **Save & Quit**.

If CCS has existing configurations in **My System**, click **Launch Setup** under **File** pull-down menu. Select **Remove All** to remove the existing configurations; then select **ARM7 SIMULATOR BIG ENDIAN** as shown in [Figure 24](#), Click **ADD** and then **Save & Quit** for UCD3138 device.

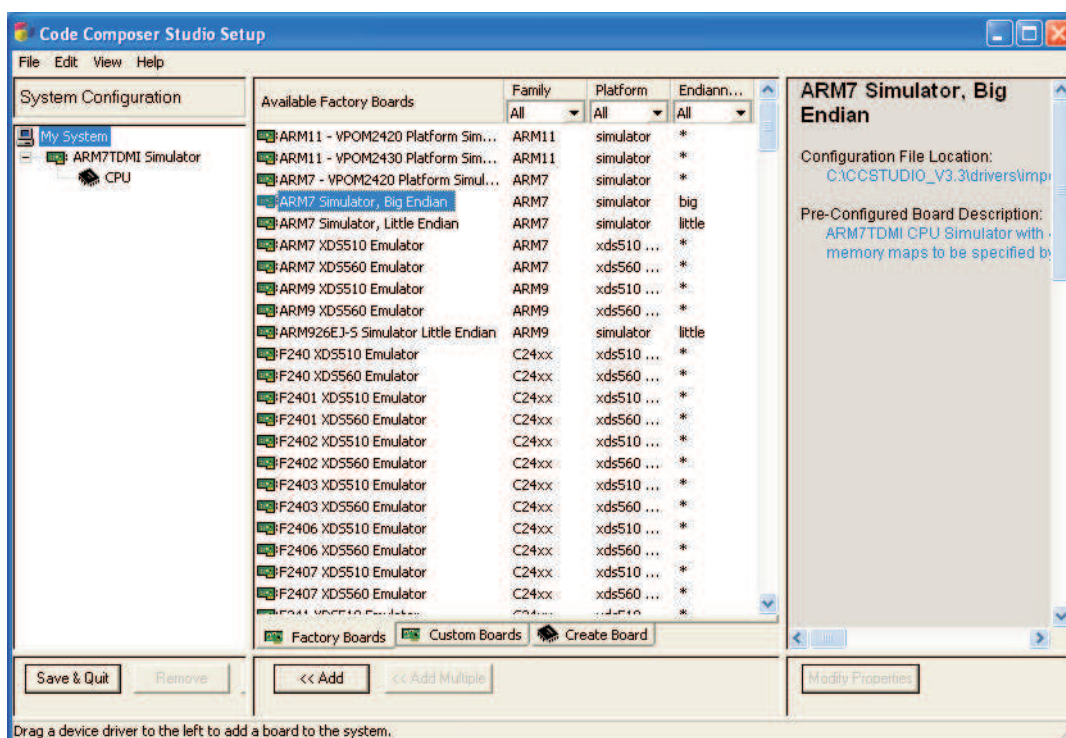


Figure 24. Set up Code Composer Studio v3.3 for UCD3138

## A.2 Build and Compile a Project using Code Composer Studio

After a project is created with all source codes developed, one can compile the project using CCS. On the matter how to create a project, please refer to reference 2, Code Composer Studio Development Tools v3.3 – Getting Started Guide. The example below is to describe typical compile process for UCD3138 firmware. The project file name is **Cyclone.pjt** in a folder named **Training 02**. The final result is a file with extension **.x0**. As the project name is **Cyclone.pjt**, the final file name **Cyclone.x0** is naturally chosen. **Cyclone.x0** is the final firmware code to be downloaded to the UCD3138 device memory for UCD3138 intended functional operation. Here are the steps for a typical compile process.

1. Copy file folder **Training 02** in any desired directory inside your PC.
2. Launch CCS and open the CCS project file **Cyclone.pjt** from the directory where **Training 02** was saved. The following window appears shown in [Figure 25](#).
3. As project **Cyclone.pjt** has been created and orientated for UCD3138 functions, CCS can be launched without connecting an emulator.
4. From CCS project window, Right click on **Cyclone.pjt (Debug)** and then select **Build Options...** the window as shown in [Figure 26](#) appears when the **Linker** tab is selected.
5. [Figure 26](#) shows the project **Build Options** have been selected to create the file **Cyclone.out** from CCS.
6. The next step is to convert file **Cyclone.out** to **Cyclone.x0**. “yclone.x0 is the final firmware code to be downloaded to the UCD3138 device memories. To convert **Cyclone.out** to **Cyclone.x0**, the first step is to click **General** tab under **Build Options for Cyclone.pjt (Debug)**, as [Figure 27](#) shown, under the **Build Command**, confirm the file to be converted is **Cyclone.out**, then click ok to close **Build Options**.
7. Steps 4, 5, and 6 are only necessary once per project. If using a TI developed firmware as a platform, these steps have been set up and no need to repeat.
8. As shown in [Figure 28](#), select **Project M Rebuild All**. This generates the file **Cyclone.x0** based on **Cyclone.out**, and the file of **Cyclone.x0** is saved inside the folder where **Cyclone.pjt** is saved.

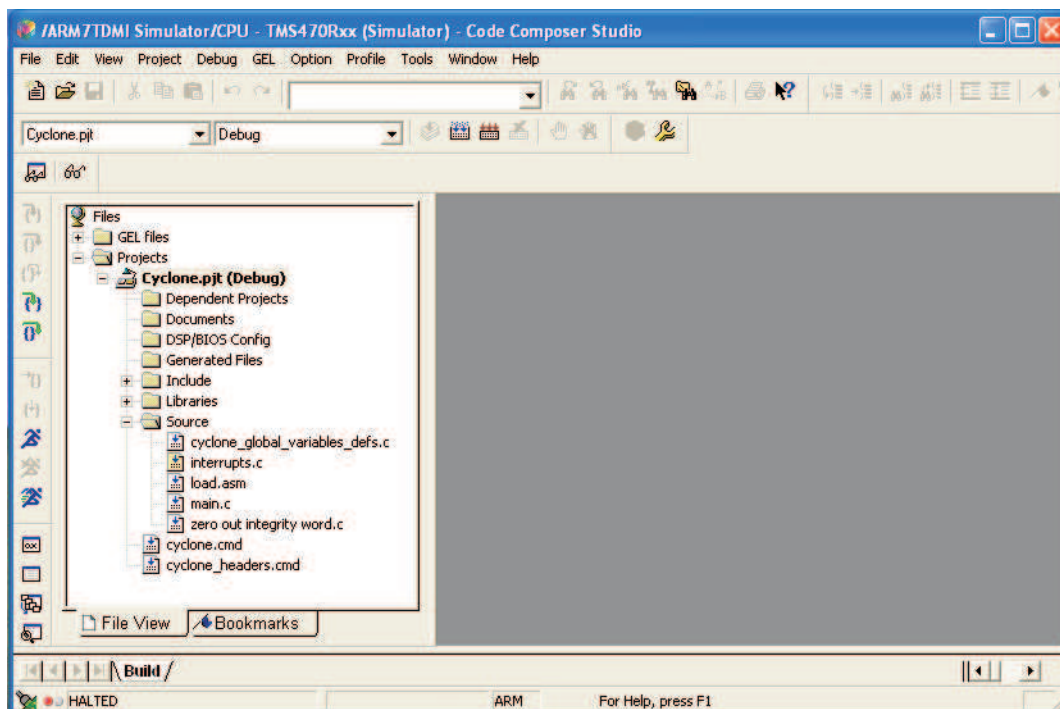


Figure 25. Open a Project File **Cyclone.pjt** (initial open)

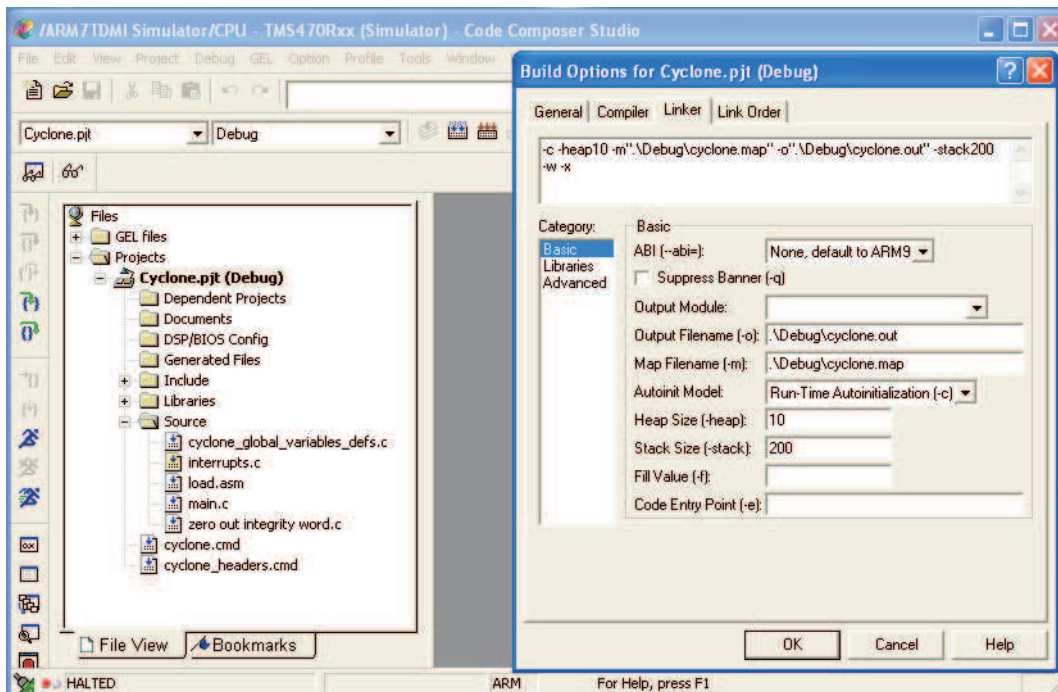


Figure 26. Open a Project File *Cyclone.pjt* (build options and linker tab)

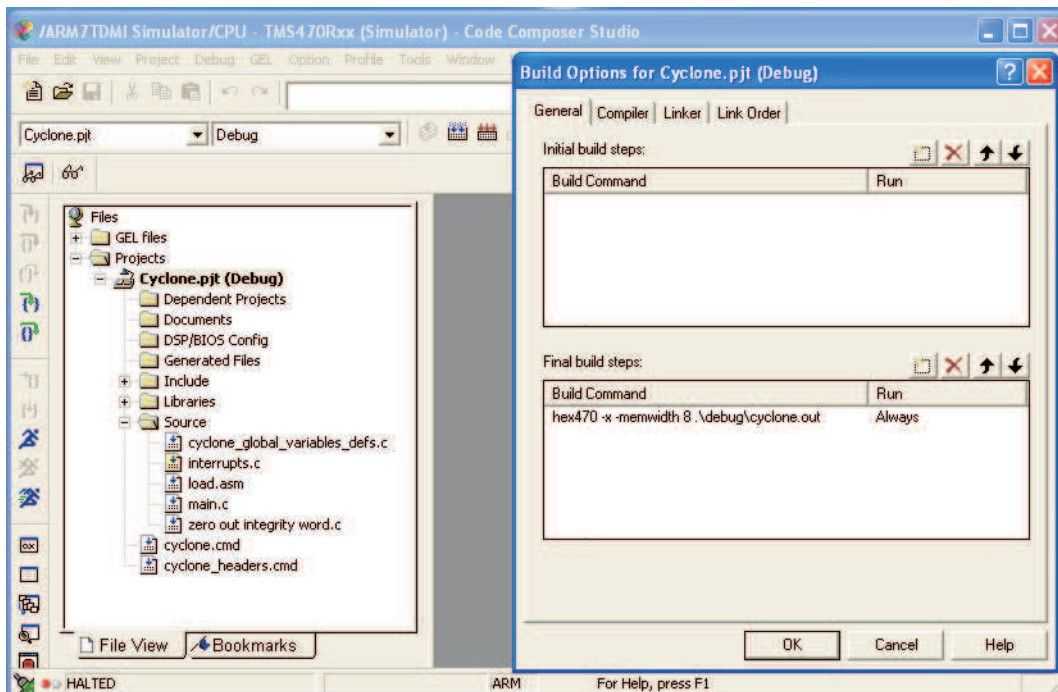
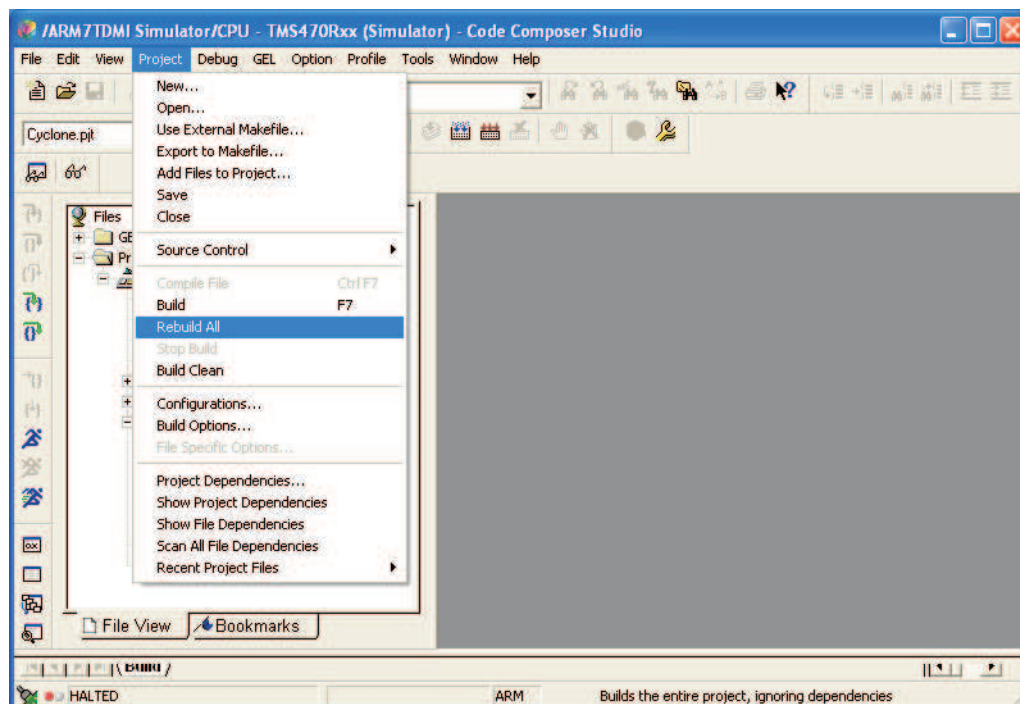


Figure 27. Open a Project File *Cyclone.pjt* (build options and general tab)





**Figure 28. Open a Project File *Cyclone.pjt* (rebuild all)**

### A.3 References

1. UCD3138 Datasheet, [Highly Integrated Digital Controller for Isolated Power](#), (Texas Instruments Literature Number SLUSAP2), 2012
2. UCD3138CC64EVM-030 Evaluation Module and User's Guide, [Programmable Digital Power Controller Control Card Evaluation Module](#), (Texas Instruments Literature Number SLUU886), 2012
3. TI Application Manual, [UCD3138 Digital Power Peripherals Programmer's Manual](#), (Texas Instruments Literature Number SLUU995)
4. TI Application Manual, [UCD3138 Monitoring and Communications Programmer's Manual](#), (Texas Instruments Literature Number SLUU996)
5. TI Application Manual, [UCD3138 ARM and Digital System Programmer's Manual](#), (Texas Instruments Literature Number SLUU994)
6. User Guide, *UCD3138 Isolated Power Fusion GUI*, (please contact TI)

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### FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

### **FCC Interference Statement for Class B EVM devices**

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

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Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.



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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
3. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.
4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

**Certain Instructions.** It is important to operate this EVM within TI's recommended specifications and environmental considerations per the user guidelines. Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output are maintained at a normal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch. As with all electronic evaluation tools, only qualified personnel knowledgeable in electronic measurement and diagnostics normally found in development environments should use these EVMs.

**Agreement to Defend, Indemnify and Hold Harmless.** You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of the agreement. This obligation shall apply whether Claims arise under law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

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