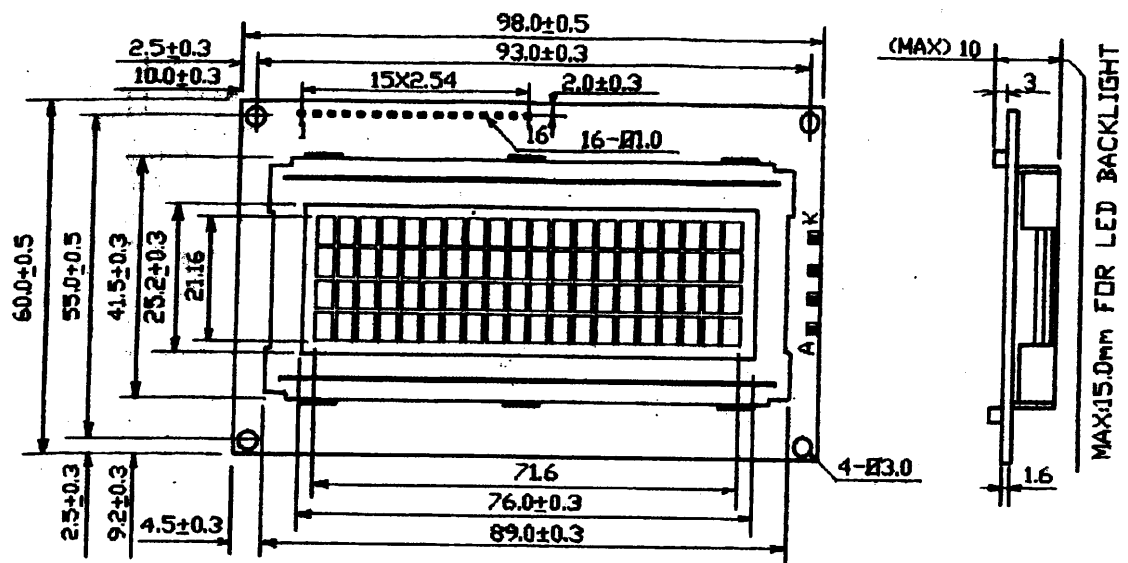


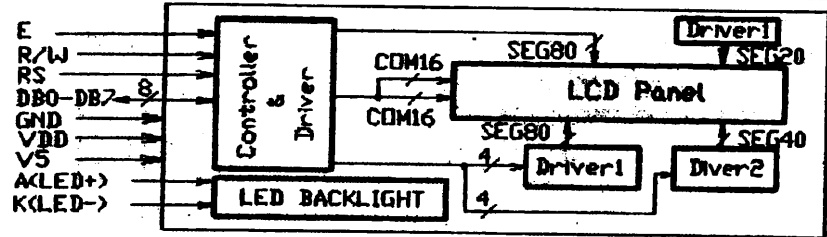
142 580

TRI MODS-1546 TN NON B/L

20 X 4 CHARACTER MODULE



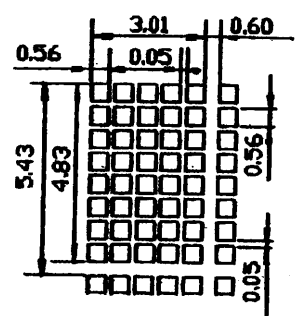
Block Diagram & DD RAM Address



	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Line1	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10	11	12	13
Line2	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
Line3	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
Line4	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67

RAM area : 00H — 27H and 40H — 67H
Note : Please select 2 line display

Display Pattern



Interface Pin Function

Pin No.	Symbol	Level	Function
1	Vss	—	Power Supply
2	Vdd	—	
3	Vee	—	
4	Rs	H/L	Register select H:Data Input L:Instruction Input
5	R/W	H/L	Read/Write select H:Data Read(Module-MPU) L:Data Write(Module-MPU)
6	E	H/L	Operation start signal for data read/write
7	DB0	H/L	Lower order 4 line data bus with bidirectional three--state. Used for data transfer between the MPU and the module. These four are not used during 4-bit operation.
8	DB1	H/L	
9	DB2	H/L	
10	DB3	H/L	
11	DB4	H/L	Higher order 4 lines data bus with bidirectional three--state. Used for data transfer between the MPU and the module. DB7 can be used as a BUSY flag
12	DB5	H/L	
13	DB6	H/L	
14	DB7	H/L	
15	(K)LED+	—	Supply Voltage for LED Backlighting
16	(K)LED-	—	
			0V(GND)

Note : * marked pin are only for LED backlighting type